



K J's EDUCATIONAL INSTITUTES

Trinity College of Engineering and Research, Pune



An Autonomous Institute Affiliated to Savitribai Phule Pune University
Approved by AICTE & Government of Maharashtra

VISION AND MISSION



To be a premier knowledge center
for socio-economic development



To provide education that
combines rigorous academics
with joy of discovery through
sustained efforts and dynamic
strategies in building innovative,
participatory, problem based
learning practices and research
that leads to capacity building of
students



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**Trinity College of Engineering & Research,
Pune, Maharashtra, India**

NAAC Accredited with 'A+' Grade



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Maharashtra, India**

**National Education Policy (NEP)-2020 Compliant
Curriculum**

**F. Y. M. Tech. Electronics (Digital Systems)
(2025 Pattern)**

(With effect from Academic Year 2025-26)

NEP 2020 Compliant Curriculum Structure
F. Y. M. Tech. Electronics (Digital Systems) (2025 Pattern)

Course Code	Course Type	Course Name	Teaching Scheme (Hrs./week)		Examination Scheme and Marks					Credits		
			Theory	Practical	CIE	ESE	Practical	Oral	Total	Theory	Oral	Total
Semester I												
MCCM3501	Major Core Course	Embedded System Design	4	-	50	50	-	-	100	4	-	4
MCCM3502	Major Core Course	Advanced Multirate Signal processing	4	-	50	50	-	-	100	4	-	4
MCCM3503	Major Core Course	Analog and Digital CMOS Design	3	-	50	50	-	-	100	3	-	3
MCCM3504	Major Core Course	Lab Practice –I	-	4	-	-	-	50	50	-	2	2
MECM3521	Major Elective Course	Elective I	4	-	50	50	-	-	100	4	-	4
HSSMM3561	Humanities social science and Management (Mandatory Course)	Human Rights	1	-		50	-	-	50	1	-	1
RMM3541	RM	Research Methodology	4	-	50	50	-	-	100	4	-	4
		Total	20	4	250	300	-	50	600	20	2	22

List of Elective I Courses:

MECM3521A	Artificial Neural Network and Application
MECM3521B	Information Theory and Coding
MECM3521C	Wireless Mobile Technology
MECM3521D	Internet of Things

NEP 2020 Compliant Curriculum Structure
F. Y. M. Tech. Electronics (Digital Systems) (2025 Pattern)

Course Code	Course Type	Course Name	Teaching Scheme (Hrs./week)		Examination Scheme and Marks					Credits		
			Theory	Practical	CIE	ESE	Practical	Oral	Total	Theory	Oral	Total
Semester II												
MCCM3505	Major Core Course	Advanced DSP Processors	4	-	50	50	-	-	100	4	-	4
MCCM3506	Major Core Course	Image Processing and Computer Vision	4	-	50	50	-	-	100	4	-	4
MCCM3507	Major Core Course	System on Chip Design	3	-	50	50	-	-	100	3	-	3
MCCM3508	Major Courses	Lab Practice -II	-	4	-	-	-	50	50	-	2	2
MECM3522	Major Elective Course	Elective –II	4	-	50	50	-	-	100	4	-	4
HSSMM3562	Humanities social science and Management (Mandatory Course)	Universal Human values	1	-	-	50	-	-	50	1	-	1
ELM3542	Experiential learning	Seminar 1	-	8	50	-	-	50	100	-	4	4
		Total	16	12	250	250	-	100	600	16	6	22

List of Elective II Courses:

MECM3522A	Research Writing and IPR
MECM3522B	Deep Learning and Applications
MECM3522C	Digital Business Analytics
MECM3522D	Fault tolerance system and simulation